

#13 7-11-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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09,388,826

Application Serial No.
Filing Date September 11, 1999
Inventor Weimin Li, et al.
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner E. Kielin
Attorney's Docket No. MI22-1208
Title: Low k Interlevel Electric Layer Fabrication Methods

INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56.
Copies of the cited prior art references are attached. No admission is made regarding whether the submitted references are prior art.


This Supplemental Information Disclosure Statement is being filed within three months of the filing date of a Request for Continued Examination or before the mailing of a first Office Action subsequent thereto, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 CFR § 1.17(p) to Deposit Account No. 23-0925.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 6-14-01

By:


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Form PTO 1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1208	SERIAL NO. 09,388,826
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Weimin Li et al.	
				FILING DATE September 1, 1999	GROUP 2813

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U.S. PATENT DOCUMENTS							
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